

Please replace the Abstract with the following:

ABSTRACT OF THE DISCLOSURE

5 A reconfigurable processor system in an integrated circuit includes a processor core that operates on a set of instructions to carry out predefined processes. A plurality of input/output pins are provided for interfacing with external signals. A reconfigurable interface interfaces between the processor core and the input/output pins through select ones of a plurality of functional blocks. The reconfigurable interface is operable to define how each of the plurality of input/output pins interfaces with the processor core and the functionality associated therewith. The functional blocks provide the interface of the processor core with the input/output pins.

AMENDMENT AND RESPONSE

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